J?
Bus_ISA_8bit
U20
74LS245
U13
74LS244
U11D
74LS02
LATCH 16 ADDRESS BITS
FOR LOOPBACK TEST
U15A
74LS21
U9B
74LS00
Memory write Fxxxx:xxxx
U19
74LS374
U18
74LS374
J?
DC62_Female
ADDRESS BUFFERS
U21
74LS244
U22
74LS244
U23
74LS244
IRQ, IOERR, ZERO WAIT
U24B
74LS32
U12C
74LS09
U24A
74LS32
U12B
74LS09
U24C
74LS32
U12D
74LS09
ADDRESS DECODER
IO Address = 1110 xx10 0001 1001 = Ex19
U15B
74LS21
U5D
74LS32
U11B
74LS02
U11C
74LS02
U9A
74LS00
U3D
74LS04
U2C
74LS08
U2D
74LS08
U8
74LS138
REGISTER SELECT
U3A
74LS04
U10
74LS155
CARD ENABLE REGISTER
U3B
74LS04
U3E
74LS04
U24D
74LS32
U3F
74LS04
U1A
74LS74
LOOPBACK TEST REGISTER
U17
74LS374
STATUS REGISTER
U14
74LS244
WAIT STATE MACHINE
U9D
74LS00
U2B
74LS08
U2A
74LS08
U1B
74LS74
U4A
74LS74
U12A
74LS09
U7B
74LS74
DATA BUFFER CONTROL
U11A
74LS02
U5A
74LS32
U9C
74LS00
U5B
74LS32
U5C
74LS32
U6A
74LS10
U6B
74LS10
U6C
74LS10
U4B
74LS74
MISC BUFFER
U16
74LS244
PULLUPS
R3
314A822
R1
316E221331
R2
314A822
ZR1
2k
U7A
74LS74
POWER CONNECTIONS
U1C
74LS74
U2E
74LS08
U3G
74LS04
U4C
74LS74
U5E
74LS32
U6D
74LS10
U7C
74LS74
U9E
74LS00
U11E
74LS02
U12E
74LS09
U15C
74LS21
U24E
74LS32
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